



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Sayeed Ahmed et al.

Application No.

10/658,804

Filed

September 9, 2003

For

TRI-LEVEL INVERTER

Art Unit

: 2838

Docket No.

: 130209.508

Date

: January 28, 2004

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached form PTO-1449. Applicants note that since the application filing date is after June 30, 2003, copies of the cited U.S. patents and published patent applications are not required and accordingly have not been provided. Copies of all other cited references are enclosed. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Commissioner is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

Frank Abramonte

Registration No. 38,066

Enclosures:

Form PTO-1449 Cited References (18)

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FORM PTO-1449 (REV.7-80)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. APPLICATION NO. 130209.508 10/658,804

APPLICANTS

Sayeed Ahmed et al.

INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)

FILING DATE GROUP ART UNIT
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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 AQ	Kim, T-J., et al., "The Analysis of Conduction and Switching Losses in Multi-Level Inverter System," <i>IEEE</i> , pp. 1363-1368, 2001.
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BL	Operational Limitations," <i>IEEE Industrial Applications Society Annual Meeting</i> , New
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	DI	60/233,994		Maly et al.			09/20/00
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	DN						

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